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48134 7590 07/29/2005

SLATER & MATSIL LLP
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10/19/2005 TBESHAW2 00000043 501065 10625962

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Kristin R. Hayes	(Depositor's name)
<i>K. Hayes</i>	(Signature)
October 18, 2005	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10625962	07/24/2003	Peter Poehrmueller	INTECH 3.0-097	1620

TITLE OF INVENTION: ARRAY TRANSISTOR AMPLIFICATION METHOD AND APPARATUS FOR DYNAMIC RANDOM ACCESS MEMORY

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	10/31/2005
EXAMINER	ART UNIT	CLASS-SUBCLASS			
MAI, SON LUU	2827	365-208000			

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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1. *Slater & Matsil, L.L.P.*

2. _____

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PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

Infineon Technologies AG

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Munich, Germany

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

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Date October 18, 2005

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FACSIMILE COVER SHEET

To: Commissioner for Patents	Total Pages Sent: 3
Mail Stop Issue Fee	(including cover sheet)
Facsimile Number: (571) 273-2885	Transmission Date: October 18, 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Poechmueller	Docket No.:	INTECH 3.0-097 (2003 P 50758 US)
Serial No.:	10/625,962	Art Unit:	2827
Date Filed:	July 24, 2003		
Title:	Array Transistor Amplification Method and Apparatus for Dynamic Random Access Memory		

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- Certification of Facsimile Transmission (1 page)
- Part B – Fee Transmittal (2 duplicate pages)

Respectfully submitted,

Kristin R. Hayes
Legal Assistant

Confirmation Respectfully Requested

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